

SYED HAIDER ABBAS

COMPUTER ENGINEER

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Objective

I'm a Computer Engineer with a strong interest in Embedded System Design, Hardware integration, problem-solving, and emerging technologies. I enjoy working in collaborative environments to build impactful and efficient solutions.

Internship Experience

Computer Engineering Department, UET Taxila

Taxila, Pakistan

Embedded System Design Intern at the Computer Engineering Department, UET Taxila, where I contributed to the design and development of a smart technology-based product during a 6-week internship.

Project Intern: (July to Aug - 2024)

Green Vision

Remote, Pakistan

Completed a remote internship at Green Vision, working on Python programming and completing 100 Python tasks.

Python Programming Intern: (July to Aug- 2024)

Digital Empowerment Pakistan

Remote, Pakistan

Completed a remote internship of Machine Learning at Digital Empowerment Pakistan.

Machine Learning Intern: (July 1st to July 31st- 2024)

Engineering Projects

Smart Cushion (Final Year Project)

Developed a smart cushion system integrating embedded sensors and IoT technology for healthy posture monitoring.

Skills: Embedded Systems, IoT, Sensor Integration, C++ Language, Data Analysis, Machine Learning, Emotion Recognition techniques, Dataset Acquisition.

Temperature and Humidity Detection By using ESP32

Integrated microcontroller and sensor circuits with ESP32 to monitor and transmit temperature and humidity data over specific area in real time.

Skills: IoT, Microcontroller Programming (ESP32), Sensor Integration, Data Transmission.

Gender Recognition System using Voice Speech in Matlab

Developed a digital signal processing system in Matlab to analyze voice signals and accurately identify the gender of the speaker.

Skills: Digital Signal Processing, Matlab, Voice Recognition, Signal Analysis.

Voice Over IP System (Cisco Packet Tracer)

Designed and simulated a secure communication system between devices over an IP network using Cisco Packet Tracer.

Skills: Computer Networking, Network Security, Cisco Packet Tracer, Secure Communication.

Stop-Watch Using FPGA

Designed and implemented a stopwatch using FPGA to accurately track and display time.

Skills: FPGA Programming, Digital System Design, Timing Circuits, VHDL/Verilog.

Obstacle Avoiding Car Robot

Built an autonomous robot that avoids obstacles using sensors and basic control algorithms.

Skills: Robotics, Sensor Integration, Embedded Systems, Arduino C Programming, Digital Logic Design.

Education

University of Engineering and Technology, Taxila

BS Computer Engineering --- 3.08/4.00

Superior Group of College for Boys, Jauhrabad (BISE Sargodha)

Intermediate- FSC (Pre Engineering) --- 1039/1100 [94.8%]

The Educators Khushab Campus, Jauhrabad (BISE Sargodha)

Matriculation- Science --- 825/1100 [75%]